

Amendments to the Specification:

Please replace the paragraph beginning on line 20, page 5, with the following amended paragraph:

Referring now to FIG 2, a block diagram illustrating selected elements of communication link 106 is shown. In the depicted embodiment, communication link 106 is a transceiver that includes a transmitter 130 and a receiver 150. For purposes of simplicity, we will describe transceiver 106 as containing a transmitter and a receiver. It will be appreciated, however, that the transmitter and the receiver will often be in separate substrates. For example, the transmitter could be part of 106, and the receiver could be part of 126. Transmitter 130 includes a transmit interface 132 that converts received data to serial data suitable for transmission over channel 110. Data received by transmitter 130 is typically parallel, digital data having CMOS voltage levels. In such a case, transmit interface 132 converts the parallel data to serial data and typically converts the CMOS voltage levels to voltage levels and logic formatting suitable for transmission over transmission channel 110. In one embodiment of particularly widespread application in high speed serial links, transmit interface 132 generates serial data in a non-return to zero (NRZ) format desirable for its lower bandwidth requirements. In addition, the transmit-side clock signal is implicitly embedded with the data produced by transmit interface 132 to conserve the number of interconnects required.

Amendments to the Drawings:

The attached sheet of drawings includes changes to FIG 4. This sheet, which includes FIG 4, replaces the original sheet that included FIG 4.

Attachment: Replacement Sheet
Annotated Sheet Showing Changes